

DLEVFH OSCILLATOR



Specifications

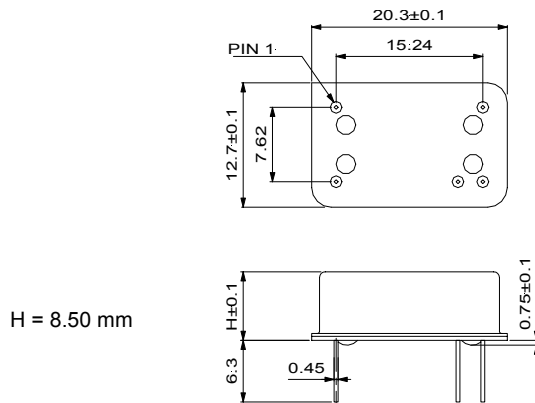
Parameters	Standard	Options
Frequency range	622 MHz to 800 MHz	
Logic output	LVPECL	
Frequency stability		
Overall		
Initial at 25°C	± 10 ppm	
Temperature	± 50 ppm	± 20 ppm best possible
vs supply voltage change	± 3 ppm	
vs load change	± 0,5 ppm	
Aging first year	± 2 ppm	
Operating temperature	0° C ~ +70°C	-20°C ~ +70°C -40°C ~ +85°C Other on custom request
Storage temperature	-40°C ~ +85°C	-55°C ~ +125°C
Supply voltage	3,3 V +5%	
Current	60 mA max	
Load	Vcc-2,0 V into 50 ohm	
Rise/fall time	0,5 ns max	
Duty cycle	45/55 %	
Jitter RMS (12kHz ~ 5,0 MHz)	0,08 ps typ , 0,10 ps max.	
Jitter RMS (12kHz ~ 20,0 MHz)	0,12 ps typ , 0,15 ps max.	
Jitter RMS (50kHz ~ 80,0 MHz)	0,32 ps typ , 0,40 ps max.	
Start-up time	10 ms max.	
Linearity	± 10% max.	
Frequency tuning range	±100 ppm min	
Pulling slope	Positive	
External control voltage	+ 1,65 VDC ±1,35 VDC	
Marking	Freq + Code + Week +Year	

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Drawing

Package	DIL14 5pin	
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Pin configuration	#1: V control	
	#7 :GND	
	#8 : Output	
	#9 : Complementary output	
	#14 : Vcc	